Selection and Implementation of a HW/SW Co-Design Environment

Task

This thesis will give a short introduction into different hardware/software-CoDesign approaches. You will present different languages and tools. Further on, you will take a closer look at the hw/sw-CoDesign using SystemC and carry out a comparison with a classical design. In this context, you shall implement an exemplary step-motor controller which is executed on a combination of microcontroller and CPLD.

HW/SW-CoDesign is meant in terms of the collective design of:

- SW for a CPU/microcontroller
- Structure of a PLD (CPLD/FPGA)
- a corresponding interface

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