

Hardware Programming

Content

In this lab course, we will present hardware programming concepts. We will discuss the following topics amongst others:

- Bootloader
- Scheduler
- Interrupts/Polling
- Input and output connection
- Memory management
- Processor connection to peripheral components, e.g. LCD display or external memory unit

All relevant material will be published on the L²P platform.

Registration

- You must register in order to participate in this course!
- Registration start: 2008-06-30, 09:00:00pm
- Registration end: 2008-08-15, 23:59:59pm
- Registration is done in [Campus-Office](#).

Lab Course Preparation

There will be an obligatory introduction to this lab course on October 16th at 10:15am in Fo 4.

The documents for each upcoming experiment session will be published in L²P prior to the respective experiment. Print out the documents and work through the respective tasks in order to prepare yourself for the experiment session.

If you have not prepared yourself, you MAY NOT participate due to safety reasons!

Location

- Room 4U18 in the “1. Erweiterungsgebäude” (E1).
- Floor plan: [Computer Science Center](#)

Board Layout

- Here you can find the design file and the component list of the evaluation board used in the lab

course. There is no need to get this board if you just want to pass the course. We have published the file due to student request.

- Design file and list of components

Links

- [CAMPUS LINK](#)
- [L²P](#)

Tutors

- [Dr.-Ing. André Stollenwerk](#)
- [Dr. rer. nat. Ralf Mitsching](#)

From:
<https://embedded.rwth-aachen.de/> - **Informatik 11 - Embedded Software**

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Last update: **2011/11/21 17:32**

