

# Dr.rer.nat. Bastian Schlich



## Publikationen

[BSK+15]

[PDFBIB](#)

Biallas, S., Simon, H., Kowalewski, S., Hauck-Stattelmann, S., and Schlich, B., "Automatische Testfallgenerierung für SPS-Programme mittels Zeilenüberdeckung", in *Proc. Automation 2015 : 16. Branchentreff der Mess- und Automatisierungstechnik, 11. und 12. Juni 2015, Baden-Baden / VDI/VDE-Gesellschaft Mess- und Automatisierungstechnik*, Düsseldorf, 2015 in VDI-Berichte, VDI-Verl., pp. 95-106.

## Automatische Testfallgenerierung für SPS-Programme mittels Zeilenüberdeckung

### Bibtex entry :

```
@inproceedings { BSK+15,
  author = { Biallas, Sebastian and Simon, Hendrik and Kowalewski,
    Stefan
      and Hauck-Stattelmann, Stefan and Schlich, Bastian },
  title = { Automatische Testfallgenerierung für SPS-Programme
    mittels Zeilenüberdeckung },
  booktitle = { Automation 2015 : 16. Branchentreff der Mess- und
    Automatisierungstechnik, 11. und 12. Juni 2015, Baden-Baden
    / VDI/VDE-Gesellschaft Mess- und Automatisierungstechnik },
  publisher = { VDI-Verl. },
  pages = { 95-106 },
  series = { VDI-Berichte },
  year = { 2015 },
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  typ = { PUB:(DE-HGF)7 },
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reportid = { RWTH-CONV-207912 },
cin = { 122810 / 120000 },
url = { http://publications.embedded.rwth-aachen.de/file/5r },
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[HBS+15]

[PDFBIB](#)

Hauck-Stattelmann, S., Biallas, S., Schlich, B., Kowalewski, S., and Jetley, R., "Analyzing the Restart Behavior of Industrial Control Applications", in *Proc. FM 2015: formal methods : 20th international symposium, Oslo, Norway, June 24 - 26, 2015 ; proceedings / Nikolaj Bjørner; Frank de Boer (eds.)*, Springer International Publishing, 2015 in *Lecture Notes in Computer Science*, Cham, pp. 585-588.

## Analyzing the Restart Behavior of Industrial Control Applications

### Bibtex entry :

```
@inproceedings { HBS+15,
  author = { Hauck-Stattelmann, Stefan and Biallas, Sebastian and
    Schlich, Bastian and Kowalewski, Stefan and Jetley, Raoul },
  title = { Analyzing the Restart Behavior of Industrial Control
    Applications },
  booktitle = { FM 2015: formal methods : 20th international
symposium,
    Oslo, Norway, June 24 - 26, 2015 ; proceedings / Nikolaj
    Bjørner; Frank de Boer (eds.) },
  publisher = { Cham },
  pages = { 585-588 },
  series = { Lecture Notes in Computer Science },
  year = { 2015 },
  address = { Springer International Publishing },
  organization = { 20. International Symposium Formal Methods, Oslo
(Norway),
    2015-06-24 - 2015-06-26 },
  doi = { 10.1007/978-3-319-19249-9_38 },
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  cin = { 122810 / 120000 },
  url = { http://publications.rwth-aachen.de/record/541096 },
}
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[SFB+15]

[PDFBIB](#)

Simon, H., Friedrich, N., Biallas, S., Hauck-Stattelmann, S., Schlich, B., and Kowalewski, S., "Automatic test case generation for PLC programs using coverage metrics", in *Proc. 2015 IEEE 20th Conference on Emerging Technologies Factory Automation (ETFA) : 8 - 11 Sept. 2015, City of Luxembourg, Luxembourg / co-sponsored by IEEE ...*, Piscataway, NJ, 2015, IEEE, p. 4.

# Automatic test case generation for PLC programs using coverage metrics

## Bibtex entry :

```
@inproceedings { SFB+15,  
  author = { Simon, Hendrik and Friedrich, Nico and Biallas,  
  Sebastian  
    and Hauck-Stattelmann, Stefan and Schlich, Bastian and  
    Kowalewski, Stefan },  
  title = { Automatic test case generation for PLC programs using  
    coverage metrics },  
  booktitle = { 2015 IEEE 20th Conference on Emerging Technologies  
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    Luxembourg / co-sponsored by IEEE ... },  
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  pages = { 4 S. },  
  year = { 2015 },  
  address = { Piscataway, NJ },  
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  doi = { 10.1109/ETFA.2015.7301602 },  
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}
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[BKS+14]

[PDFBIB](#)

Biallas, S., Kowalewski, S., Stattelmann, S., and Schlich, B., "Efficient Handling of States in Abstract Interpretation of Industrial Programmable Logic Controller Code", in *Proc. Discrete Event Systems : [Proc. Proceedings of the 12th International Workshop on Discrete Event Systems, Cachan, France, 2014] / Conference Editor: Lesage, Jean-Jacques, Faure, Jean-Marc, Cury, Jose E. R., Lennartson, Bengt, Cachan, France, 2014, IFAC, pp. 400-405.*

# Efficient Handling of States in Abstract Interpretation of Industrial Programmable Logic Controller Code

## Bibtex entry :

```
@inproceedings { BKS+14,  
  author = { Biallas, Sebastian and Kowalewski, Stefan and  
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  title = { Efficient Handling of States in Abstract Interpretation  
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Industrial Programmable Logic Controller Code },
booktitle = { Discrete Event Systems : [Proc. Proceedings of the
12th
International Workshop on Discrete Event Systems, Cachan,
France, 2014] / Conference Editor: Lesage, Jean-Jacques,
Faure, Jean-Marc, Cury, Jose E. R., Lennartson, Bengt },
publisher = { IFAC },
pages = { 400-405 },
year = { 2014 },
address = { Cachan, France },
organization = { Discrete Event Systems : [12th International
Workshop on
Discrete Event Systems], Cachan (France), 2014-05-14 -
2014-05-16 },
doi = { 10.3182/20140514-3-FR-4046.00065 },
typ = { PUB:(DE-HGF)8 },
reportid = { RWTH-CONV-205968 },
cin = { 120000 / 122810 },
url = { http://publications.rwth-aachen.de/record/443854 },
}
```

[SBS+14]

[PDFBIB](#)

Stattelmann, S., Biallas, S., Schlich, B., and Kowalewski, S., "Applying Static Code Analysis on Industrial Controller Code", in *Proc. 2014 IEEE [International Conference on] Emerging Technologies and Factory Automation (ETFA 2014) : Barcelona, Spain, 16 - 19 September 2014 / [co-sponsored by Universitat Politècnica de Catalunya - Barcelona Tech (UPC); IEEE Industrial Electronics Society]*, Piscataway, NJ, 2014, IEEE, p. 4.

## Applying Static Code Analysis on Industrial Controller Code

### Bibtex entry :

```
@inproceedings { SBS+14,
author = { Stattelmann, Stefan and Biallas, Sebastian and Schlich,
Bastian and Kowalewski, Stefan },
title = { Applying Static Code Analysis on Industrial Controller
Code },
booktitle = { 2014 IEEE [International Conference on] Emerging
Technologies and Factory Automation (ETFA 2014) : Barcelona,
Spain, 16 - 19 September 2014 / [co-sponsored by Universitat
Politècnica de Catalunya - Barcelona Tech (UPC); IEEE
Industrial Electronics Society] },
publisher = { IEEE },
pages = { 4 Seiten },
year = { 2014 },
address = { Piscataway, NJ },
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    2014-09-16 - 2014-09-19 },
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    cin = { 120000 / 122810 },
    url = { http://publications.rwth-aachen.de/record/444619 },
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```

[BKK+13]

[PDFBIB](#)

Biallas, S., Kamin, V., Kowalewski, S., Schlich, B., Sehestedt, S., and Stattelmann, S., "Verifikation von sicherheitsgerichteten SPS-Programmen mit Hilfe von Safety-Automaten", in *Proc. Automation 2013 : 14. Branchentreff der Mess- und Automatisierungstechnik ; Kongresshaus Baden-Baden, 25. und 26. Juni 2013 / VDI/VDE-Gesellschaft Mess- und Automatisierungstechnik*, Düsseldorf, 2013 in VDI-Berichte, VDI-Verl., pp. 75-79.

## Verifikation von sicherheitsgerichteten SPS-Programmen mit Hilfe von Safety-Automaten

### Bibtex entry :

```

@inproceedings { BKK+13,
  author = { Biallas, Sebastian and Kamin, Volker and Kowalewski, Stefan
    and Schlich, Bastian and Sehestedt, Stephan and Stattelmann, Stefan },
  title = { Verifikation von sicherheitsgerichteten SPS-Programmen mit
    Hilfe von Safety-Automaten },
  booktitle = { Automation 2013 : 14. Branchentreff der Mess- und
    Automatisierungstechnik ; Kongresshaus Baden-Baden, 25. und
    26. Juni 2013 / VDI/VDE-Gesellschaft Mess- und
    Automatisierungstechnik },
  publisher = { VDI-Verl. },
  pages = { 75-79 },
  series = { VDI-Berichte },
  year = { 2013 },
  address = { D{"u"}sseldorf },
  organization = { 14. Branchentreff der Mess- und
    Automatisierungstechnik
    (Germany), 2013-06-25 - 2013-06-26 },
  typ = { PUB:(DE-HGF)8 },
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  cin = { 120000 / 122810 },
  url = { http://publications.rwth-aachen.de/record/226206 },
}

```

[BKS12]

[PDFBIB](#)

Biallas, S., Kowalewski, S., and Schlich, B., "Automatische Wertebereichsanalyse von SPS-

Programmen", in *Proc. Automation 2012 : der 13. Branchentreff der Mess- und Automatisierungstechnik / VDI/VDE-Gesellschaft Mess- und Automatisierungstechnik ; Kongress 'Automation 2012' ; 13 (Baden-Baden) ; 2011.06.13-14* Branchentreff der Mess- und Automatisierungstechnik, Düsseldorf, 2012 in VDI-Berichte, VDI-Verl., pp. 79-83.

## Automatische Wertebereichsanalyse von SPS-Programmen

### Bibtex entry :

```
@inproceedings { BKS12,
  author = { Biallas, Sebastian and Kowalewski, Stefan and Schlich, Bastian },
  title = { Automatische Wertebereichsanalyse von SPS-Programmen },
  booktitle = { Automation 2012 : der 13. Branchentreff der Mess- und Automatisierungstechnik / VDI/VDE-Gesellschaft Mess- und Automatisierungstechnik ; Kongress 'Automation 2012' ; 13 (Baden-Baden) ; 2011.06.13-14 Branchentreff der Mess- und Automatisierungstechnik },
  publisher = { VDI-Verl. },
  pages = { 79-83 },
  series = { VDI-Berichte },
  year = { 2012 },
  address = { D{"u"}sseldorf },
  organization = { 13. Branchentreff der Mess- und Automatisierungstechnik, Baden-Baden (Germany), 2011-06-13 - 2011-06-14 },
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  cin = { 122810 / 120000 },
  url = { http://publications.rwth-aachen.de/record/120638 },
}
```

[BKS12a]

[PDFBIB](#)

Biallas, S., Kowalewski, S., and Schlich, B., "Automatische Wertebereichsanalyse -- Formale Verifikation für SPS-Programme", *Automatisierungstechnische Praxis : atp*, vol. 54, iss. 7/8, pp. 68-74, 2012

## Automatische Wertebereichsanalyse -- Formale Verifikation für SPS-Programme

### Bibtex entry :

```
@article { BKS12a,
  author = { Biallas, Sebastian and Kowalewski, Stefan and Schlich, Bastian },
  title = { Automatische Wertebereichsanalyse -- Formale Verifikation f{"u"}r SPS-Programme },
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journal = { Automatisierungstechnische Praxis : atp },
publisher = { Oldenbourg Industrieverl. },
pages = { 68-74 },
volume = { 54 },
number = { 7/8 },
year = { 2012 },
address = { M{"u"}nchen },
issn = { 0178-2320 },
typ = { PUB:(DE-HGF)16 },
reportid = { RWTH-CONV-015025 },
cin = { 122810 / 120000 },
url = { http://publications.rwth-aachen.de/record/133988 },
}
```

[BKS12b]

[PDFBIB](#)

Biallas, S., Kowalewski, S., and Schlich, B., "Range and Value-Set Analysis for Programmable Logic Controllers", in *Proc. Proceedings of the 11th International Workshop on Discrete Event Systems*, Guadalajara, Mexico, 2012, IFAC, pp. 378-383.

## Range and Value-Set Analysis for Programmable Logic Controllers

### Bibtex entry :

```
@inproceedings { BKS12b,
  author = { Biallas, Sebastian and Kowalewski, Stefan and Schlich,
    Bastian },
  title = { Range and Value-Set Analysis for Programmable Logic
    Controllers },
  booktitle = { Proceedings of the 11th International Workshop on
    Discrete
      Event Systems },
  publisher = { IFAC },
  pages = { 378-383 },
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  cin = { 122810 / 120000 },
  url = { http://publications.rwth-aachen.de/record/752302 },
}
```

[BSK12]

[PDFBIB](#)

Brauer, J., Schlich, B., and Kowalewski, S., "Parallel and distributed invariant checking of microcontroller software", *Electronic notes in theoretical computer science : ENTCS*, vol. 287, pp. 45-63, 2012

# Parallel and distributed invariant checking of microcontroller software

## Bibtex entry :

```
@article { BSK12,
  author = { Brauer, J{\\"o}rg and Schlich, Bastian and Kowalewski,
Stefan },
  title = { Parallel and distributed invariant checking of
microcontroller software },
  journal = { Electronic notes in theoretical computer science :
ENTCS },
  publisher = { Elsevier Science },
  pages = { 45-63 },
  volume = { 287 },
  year = { 2012 },
  address = { Amsterdam [u.a.] },
  issn = { 1571-0661 },
  organization = { 4. International Workshop on Numerical and
Symbolic Abstract
Domains (France), 10-09-2010 },
  doi = { 10.1016/j.entcs.2009.09.059 },
  typ = { PUB:(DE-HGF)16 },
  reportid = { RWTH-CONV-013643 },
  cin = { 122810 / 120000 },
  url = { http://publications.rwth-aachen.de/record/132534 },
}
```

[BBK+11]

[PDFBIB](#)

Biallas, S., Brauer, J., Kowalewski, S., and Schlich, B., "Automatically Deriving Symbolic Invariants for PLC Programs Written in IL", in *Proc. Formal methods for automation and safety in railway and automotive systems : FORMS/FORMAT 2010 ; [8th symposium ; proceedings] / Eckehard Schnieder; Géza Tarnai, eds.*, Heidelberg [u.a.], 2011, Springer, pp. 237-245.

## Automatically Deriving Symbolic Invariants for PLC Programs Written in IL

### Bibtex entry :

```
@inproceedings { BBK+11,
  author = { Biallas, Sebastian and Brauer, J{\\"o}rg and Kowalewski,
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  title = { Automatically Deriving Symbolic Invariants for PLC
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    proceedings] / Eckehard Schnieder; Géza Tarnai, eds. },
    publisher = { Springer },
    pages = { 237-245 },
    year = { 2011 },
    address = { Heidelberg [u.a.] },
    organization = { Formal methods for automation and safety in
railway and
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    doi = { 10.1007/978-3-642-14261-1 },
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}

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[BKS11]

[PDFBIB](#)

Biallas, S., Kowalewski, S., and Schlich, B., "Leistungsfähige Verifikation von industriellen SPS-Programmen mittels Model-Checking und statischer Analyse", in *Proc. 'Zukunft verantwortungsvoll gestalten' : Automation 2011 ; der 12. Branchentreff der Mess- und Automatisierungstechnik ; Kongress Baden-Baden, 28. und 29. Juni 2011 / VDI/VDE-Gesellschaft Mess- und Automatisierungstechnik. [Kongressleiter: Peter Adolphs ...]*, Düsseldorf, 2011 in VDI-Berichte, VDI-Verl., pp. 67-72.

## Leistungsfähige Verifikation von industriellen SPS-Programmen mittels Model-Checking und statischer Analyse

### Bibtex entry :

```

@inproceedings { BKS11,
  author = { Biallas, Sebastian and Kowalewski, Stefan and Schlich,
    Bastian },
  title = { Leistungsf{\a}hige Verifikation von industriellen
    SPS-Programmen mittels Model-Checking und statischer Analyse },
  booktitle = { 'Zukunft verantwortungsvoll gestalten' : Automation
    2011 ;
    der 12. Branchentreff der Mess- und Automatisierungstechnik
    ; Kongress Baden-Baden, 28. und 29. Juni 2011 /
    VDI/VDE-Gesellschaft Mess- und Automatisierungstechnik.
    [Kongressleiter: Peter Adolphs ...] },
  publisher = { VDI-Verl. },
  pages = { 67-72 },
  series = { VDI-Berichte },
  year = { 2011 },
  address = { D{\u}sseldorf },
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```

[RHS+11]

[PDFBIB](#)

Reinbacher, T., Horauer, M., Schlich, B., Brauer, J., and Scheuer, F., "Model checking embedded software of an industrial knitting machine", *International journal of information technology, communications and convergence*, vol. 1, iss. 2, pp. 186-205, 2011

## Model checking embedded software of an industrial knitting machine

### Bibtex entry :

```
@article { RHS+11,
  author = { Reinbacher, Thomas and Horauer, Martin and Schlich,
    Bastian
    and Brauer, J{"o}rg and Scheuer, Florian },
  title = { Model checking embedded software of an industrial
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  journal = { International journal of information technology,
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  publisher = { Inderscience Publishers },
  pages = { 186-205 },
  volume = { 1 },
  number = { 2 },
  year = { 2011 },
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  issn = { 2042-3217 },
  doi = { 10.1504/IJITCC.2011.039285 },
  typ = { PUB:(DE-HGF)16 },
  reportid = { RWTH-CONV-052838 },
  cin = { 122810 / 120000 },
  url = { http://www.inderscience.com/IJITCC },
}
```

[SBK11]

[PDFBIB](#)

Schlich, B., Brauer, J., and Kowalewski, S., "Application of Static Analyses for State-Space Reduction to the Microcontroller Binary Code", *Science of computer programming*, vol. 76, iss. 2, pp. 100-118, 2011

## Application of Static Analyses for State-Space Reduction to the Microcontroller Binary Code

**Bibtex entry :**

```
@article { SBK11,  
  author = { Schlich, Bastian and Brauer, J{"o}rg and Kowalewski,  
Stefan },  
  title = { Application of Static Analyses for State-Space Reduction  
to  
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  journal = { Science of computer programming },  
  publisher = { Elsevier },  
  pages = { 100-118 },  
  volume = { 76 },  
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}
```

[SNB+11]

[PDFBIB](#)

Schlich, B., Noll, T., Brauer, J., and Brutschy, L., "Reduction of Interrupt Handler Executions for Model Checking Embedded Software", in *Proc. Hardware and software: verification and testing : 5th International Haifa Verification Conference, HCV 2009, Haifa, Israel, October 19-22, 2009 : revised selected papers / Kedar Namjoshi ... (eds.)*, Heidelberg [u.a.], 2011 in *Lecture Notes in Computer Science*, Springer.

## Reduction of Interrupt Handler Executions for Model Checking Embedded Software

**Bibtex entry :**

```
@inproceedings { SNB+11,  
  author = { Schlich, Bastian and Noll, Thomas and Brauer, J{"o}rg  
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Checking  
  Embedded Software },  
  booktitle = { Hardware and software: verification and testing : 5th  
International Haifa Verification Conference, HCV 2009,  
Haifa, Israel, October 19-22, 2009 : revised selected papers  
/ Kedar Namjoshi ... (eds.) },  
  publisher = { Springer },  
  series = { Lecture Notes in Computer Science },  
  year = { 2011 },
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address = { Heidelberg [u.a.] },
organization = { 5. International Haifa Verification Conference,
HCV 2009,
    Haifa (Israel), 2009-10-19 - 2009-10-22 },
doi = { 10.1007/978-3-642-19237-1_5 },
typ = { PUB:(DE-HGF)8 },
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url = { http://publications.rwth-aachen.de/record/116214 },
}

```

[BFK+10]

PDFBIB

Biallas, S., Frey, G., Kowalewski, S., Schlich, B., and Soliman, D., "Formale Verifikation von Sicherheits-Funktionsbausteinen der PLCopen auf Modell- und Code-Ebene", in *Proc. Entwurf komplexer Automatisierungssysteme : EKA 2010 ; Beschreibungsmittel, Methoden, Werkzeuge und Anwendungen ; 11. Fachtagung mit Tutorium, 25. bis 27. Mai 2010 in Magdeburg, Denkfabrik im Wissenschaftshafen / ifak, Institut für Automation und Kommunikation e.V., Magdeburg; Otto-von-Guericke-Universität Magdeburg, Institut für Automatisierungstechnik. [Hrsg.: Ulrich Jumar, Eckehard Schnieder, Christian Diedrich]*, Magdeburg, 2010, ifak, pp. 49-57.

## Formale Verifikation von Sicherheits-Funktionsbausteinen der PLCopen auf Modell- und Code-Ebene

### Bibtex entry :

```

@inproceedings { BFK+10,
    author = { Biallas, Sebastian and Frey, Georg and Kowalewski,
Stefan
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    title = { Formale Verifikation von Sicherheits-Funktionsbausteinen
der
        PLCopen auf Modell- und Code-Ebene },
    booktitle = { Entwurf komplexer Automatisierungssysteme : EKA 2010
;
        Beschreibungsmittel, Methoden, Werkzeuge und Anwendungen ;
        11. Fachtagung mit Tutorium, 25. bis 27. Mai 2010 in
        Magdeburg, Denkfabrik im Wissenschaftshafen / ifak, Institut
        f{"u}r Automation und Kommunikation e.V., Magdeburg;
        Otto-von-Guericke-Universit{"a}t Magdeburg, Institut
        f{"u}r Automatisierungstechnik. [Hrsg.: Ulrich Jumar,
        Eckehard Schnieder, Christian Diedrich] },
    publisher = { ifak },
    pages = { 49-57 },
    year = { 2010 },
    address = { Magdeburg },
    organization = { Entwurf komplexer Automatisierungssysteme :
        Beschreibungsmittel, Methoden, Werkzeuge und Anwendungen,
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url = { http://publications.rwth-aachen.de/record/118924 },
}
```

[BNS10]

[PDFBIB](#)

Brauer, J., Noll, T., and Schlich, B., "Interval Analysis of Microcontroller Code using Abstract Interpretation of Hardware and Software", in *Proc. SCOPES '10 Proceedings of the 13th International Workshop on Software & Compilers for Embedded Systems : St. Goar, Germany - June 29-30, 2010*, New York, NY, 2010 in ACM Digital Library, ACM.

## Interval Analysis of Microcontroller Code using Abstract Interpretation of Hardware and Software

### Bibtex entry :

```
@inproceedings { BNS10,
  author = { Brauer, J{\\"o}rg and Noll, Thomas and Schlich, Bastian
},
  title = { Interval Analysis of Microcontroller Code using Abstract
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Workshop on
  Software & Compilers for Embedded Systems : St. Goar,
  Germany - June 29-30, 2010 },
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for
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```

[GSB+10]

[PDFBIB](#)

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@inproceedings { GSB+10,  
  author = { G{"u}ckel, Dominique and Schlich, Bastian and Brauer,  
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Microcontroller  
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Diagnostics of Electronic Circuits and Systems : April  
14–16, 2010, Vienna, Austria / Sponsored by IEEE Computer  
Society, Test Technology Techn. Council, In coop. with Dept.  
of Computer Engineering, Fac. of Informatics, Vienna Univ.  
of Techn. },  
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[Sch10]

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  author = { Schlich, Bastian },  
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  journal = { ACM transactions on embedded computing systems : TECS  
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year = { 2010 },
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url = { http://publications.rwth-aachen.de/record/169982 },
}
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[BHS09]

[PDFBIB](#)

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  author = { Brauer, J{\o}rg and Huuck, Ralf and Schlich, Bastian },
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ENTCS },
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[BSR+09]

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Thomas

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and Kowalewski, Stefan },
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[FHS+09]

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    Tapp, Michael },
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  booktitle = { SOFSEM 2009: theory and practice of computer science
: 35th
  Conference on Current Trends in Theory and Practice of
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  (eds.) },
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[KHS09]

[PDFBIB](#)

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  author = { Klein, Gerwin and Huuck, Ralf and Schlich, Bastian },
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[RBH+09]

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[RHS+09]

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[RHS09]

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[SBW+09]

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[SK09]

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[SKW09]

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    2067 },
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}

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[BKK+08]

[PDFBIB](#)

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  author = { Beckers, J{"o}rg and Kl{"u}nder, Daniel and  
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by  
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  booktitle = { Abstract state machines, B and Z : first  
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  conference, ABZ 2008, London, UK, September 16-18, 2008 ;  
  proceedings / Egon B{"o}rger ... (eds.) },  
  publisher = { Springer },  
  pages = { 112-124 },  
  volume = { 5238 },  
  series = { Lecture Notes in Computer Science },  
  year = { 2008 },  
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[HSW+08]

[PDFBIB](#)

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  author = { Herberich, Gerlind and Schlich, Bastian and Weise,  
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```

[NS08]

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third
  International Haifa Verification Conference, HVC 2007,
  Haifa, Israel, October 23 - 25, 2007 ; proceedings / Karen
  Yorav (ed.) },
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[RKH+08]

[PDFBIB](#)

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  author = { Reinbacher, Thomas and Kramer, Michael and Horauer,
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[RKH+08a]

[PDFBIB](#)

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cin = { 122810 / 120000 },
url = { http://publications.rwth-aachen.de/record/99569 },
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[Sch08]

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@phdthesis { Sch08,
author = { Schlich, Bastian },
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title = { Model checking of software for microcontrollers },
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[SGK08]

[PDFBIB](#)

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  author = { Schlich, Bastian and G{"u}ckel, Dominique and
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[SLK08]

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  author = { Schlich, Bastian and L{"o}ll, Jann and Kowalewski,
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[SK07d]

[PDFBIB](#)

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## An extendable architecture for model checking hardware-specific automotive microcontroller code

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  author = { Schlich, Bastian and Kowalewski, Stefan },
  title = { An extendable architecture for model checking
    hardware-specific automotive microcontroller code },
  booktitle = { FORMS/FORMAT 2007 - 6th symposium : formal methods
for
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    proceedings of Symposium FORMS/FORMAT 2007, Braunschweig,
    Germany, 25th and 26th January, 2007 / Eckehard Schnieder;
    Géza Tarnai (Eds.) },
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  pages = { 202-212 },
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url = { http://publications.rwth-aachen.de/record/117067 },  
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[SSK07]

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## Applying model checking to an automotive microcontroller application

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@inproceedings { SSK07,  
  author = { Schlich, Bastian and Salewski, Falk and Kowalewski,  
Stefan },  
  title = { Applying model checking to an automotive microcontroller  
application },  
  booktitle = { 2007 International Symposium on Industrial Embedded  
Systems  
: Costa da Caparica, [Lisbon], Portugal, 4 - 6 July 2007 ;  
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Systems,  
Costa da Caparica (Portugal), 2007-07-04 - 2007-07-06 },  
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[PSK06]

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@inproceedings { PSK06,
  author = { Palczynski, Jacob and Schlich, Bastian and Kowalewski,
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            der 36. Jahrestagung der Gesellschaft f{"u}r Informatik
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  publisher = { Ges. f{"u}r Informatik },
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  series = { GI-Edition : Proceedings },
  year = { 2006 },
  address = { Bonn },
  organization = { 36. Jahrestagung der Gesellschaft f{"u}r
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            Dresden (Germany), 2006-10-02 - 2006-10-06 },
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  url = { http://publications.rwth-aachen.de/record/111754 },
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[SK06a]

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            ; 15 - 19 November 2006, Paphos, Cyprus / IEEE Computer
            Society Leveraging Applications of Formal Methods,
            Verification and Validation (ISoLA 2006) },
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pages = { 466-473 },
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[SRW+06]

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    author = { Schlich, Bastian and Rohrbach, Michael and Weber,
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    title = { Model checking software for microcontrollers },
    pages = { 33 Bl. : graph. Darst. },
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[SK05b]

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author = { Schlich, Bastian and Kowalewski, Stefan },
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booktitle = { IEEE/NASA Workshop on Leveraging Applications of
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    ; proceedings of a workshop held at the Loyola College
    Graduate Center, Columbia, Maryland, USA, 23 - 24 September
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address = { [S.l.] },
organization = { IEEE/NASA Workshop on Leveraging Applications of
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[SK04]

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author = { Schlich, Bastian and Kowalewski, Stefan },
title = { C Model Checker: Eine {"U}bersicht },
year = { 2004 },
typ = { PUB:(DE-HGF)29 },
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